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Attorney's Docket No. S01022/80845

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven FIRTH and William Bryan BARNES  
Serial No.: 10/068,307  
Filing Date: February 6, 2002  
For: BUILT-IN TEST SUPPORT FOR AN INTEGRATED CIRCUIT

Examiner: Unassigned  
Art Unit: 2858

Box Missing Parts  
Commissioner for Patents  
Washington, D.C. 20231

STATEMENT FILED PURSUANT TO THE DUTY OF  
DISCLOSURE UNDER 37 CFR §1.56, 1.97 AND 1.98

Sir/Madam:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement is being filed within three months of the filing date of a National Application. No fee or certification is required.

Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references. attention is hereby drawn to the attached European search report.

REMARKS

Documents cited on the attached form PTO-1449 (modified) are enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;

2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;

3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

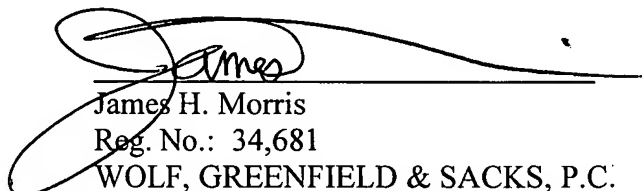
I hereby certify that this document is being placed in the United States mail with first-class postage attached, addressed to the Commissioner for Patents, Washington, D.C. 20231 on April 29, 2002.



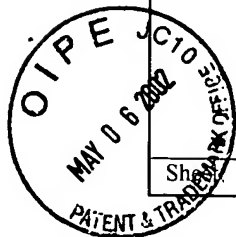
Attorney Docket No.: S01022/80845

Respectfully submitted,

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FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 10/068,307	ATTY. DOCKET NO.: S01022/8084
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: February 6, 2002	
		APPLICANT: Steven Firth and William Bryan Barnes	
		GROUP ART UNIT: 2858	EXAMINER: Unassigned
Sheet	1	of	1

## U.S. PATENT DOCUMENTS

Examiner's Initials#	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YY
		Number	Kind Code		

## FOREIGN PATENT DOCUMENTS

Examiner's Initials#	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YY	Translatio (Y/N)	
		Office/ Country	Number	Kind Code				
		EP	0 646 867	A		04/05/95		
		EP	0 805 460	A		11/05/97		

## OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials#	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		European Search Report from European application No. 01301092, filed February 7, 2001	
		<u>Array Built-In Self-Test with Diagnostic Logic</u> ; IBM Technical Disclosure Bulletin, US, IBM Corp. New York; vol. 34, no. 1, June 1, 1991; pages 59-61, XP000210114	
		<u>Semiconductor Manufacturing Process Monitoring Using Built-In Self-Test for Embedded Memories</u> ; International Test Conference, New York, NY, IEEE, vol., Conf. 29, October 19, 1998, pages 872-881, XP000822431	

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_\_\_\_, filed \_\_\_\_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]